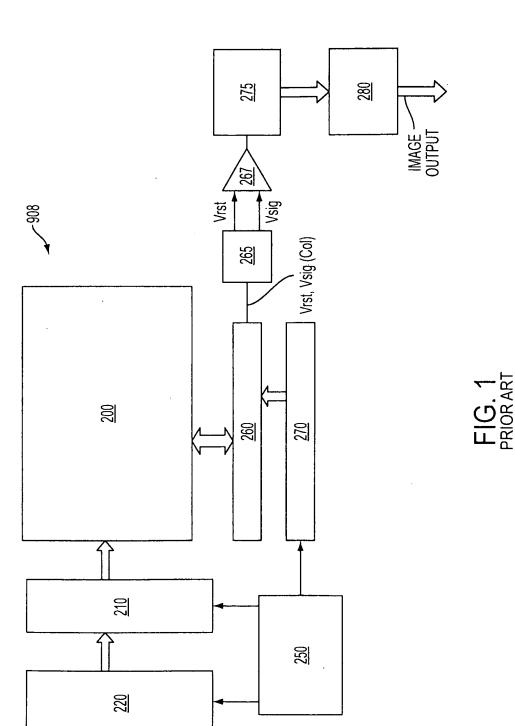
App No.: 10/721,190 Docket No.: M4065.0852/P85 nventor: Peter P. Altice, Jr. et al. Fitle: CMOS IMAGER WITH A CAPACITIVE STORAGE NODE LINKED TO TRANSFER GATE Sheet 1 of 12 REPLACEMENT SHEET

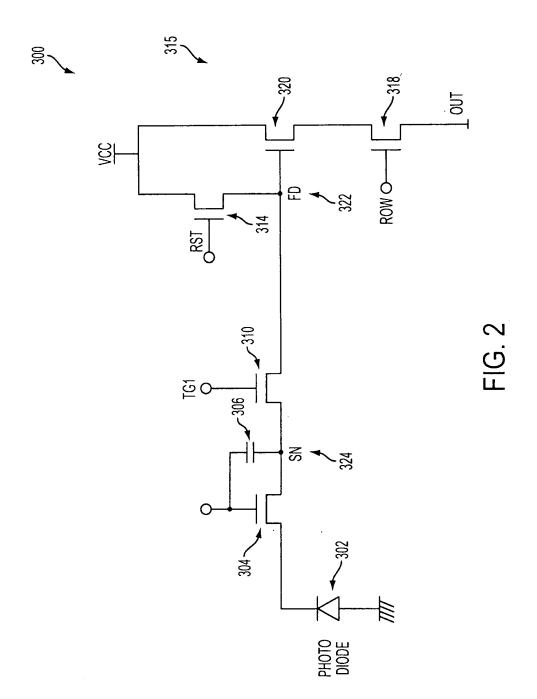




Docket No.: M4065.0852/P852 App No.: 10/721,190 Docket No.: M4065.0852/P85.
Inventor: Peter P. Altice, Jr. et al.
Title: CMOS IMAGER WITH A CAPACITIVE STORAGE NODE LINKED TO
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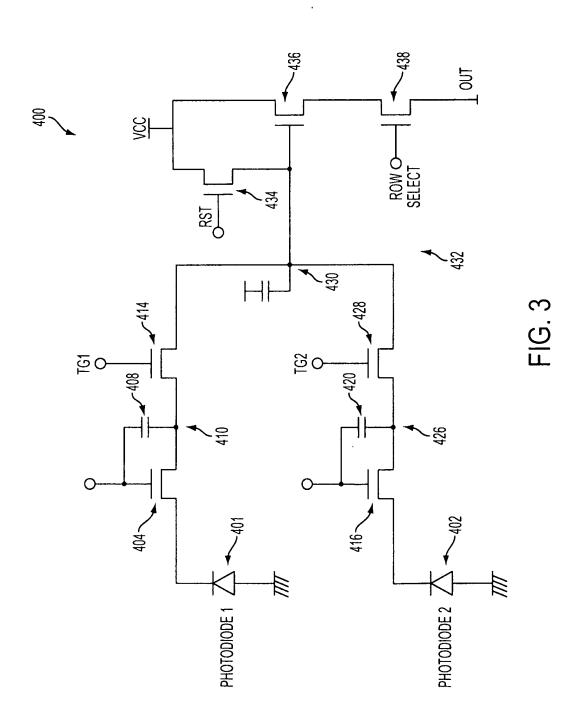
REPLACEMENT SHEET

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REPLACEMENT SHEET

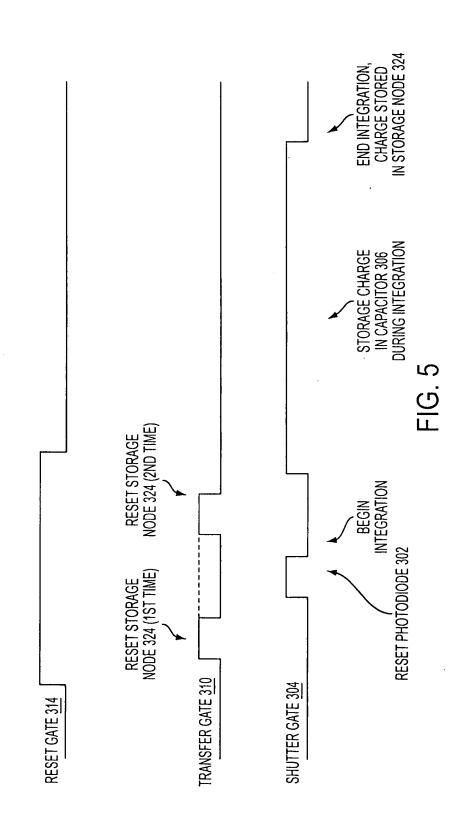
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Fitle: CMOS IMAGER WITH A CAPACITIVE STORAGE NODE LINKED TO
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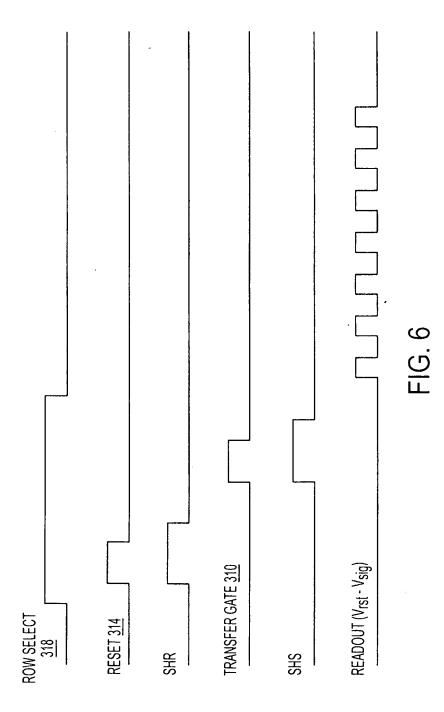


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Inventor: Peter P. Altice, Jr. et al.
Title: CMOS IMAGER WITH A CAPACITIVE STORAGE NODE LINKED TO TRANSFER GATE

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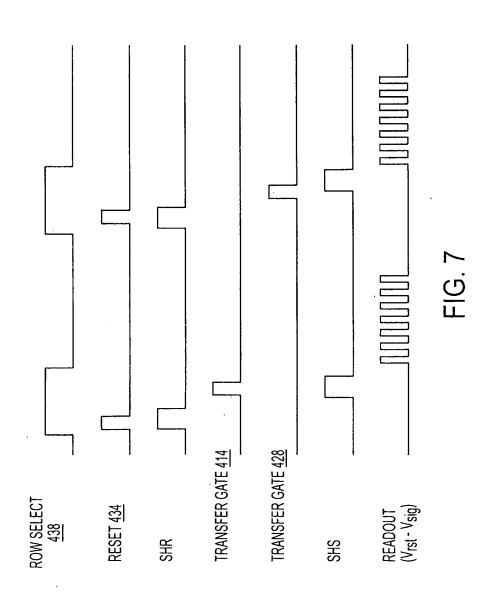
App No.: 10/721,190 Docket No.: M4065.0852/P85
nventor: Peter P. Altice, Jr. et al.

Fitle: CMOS IMAGER WITH A CAPACITIVE STORAGE NODE LINKED TO
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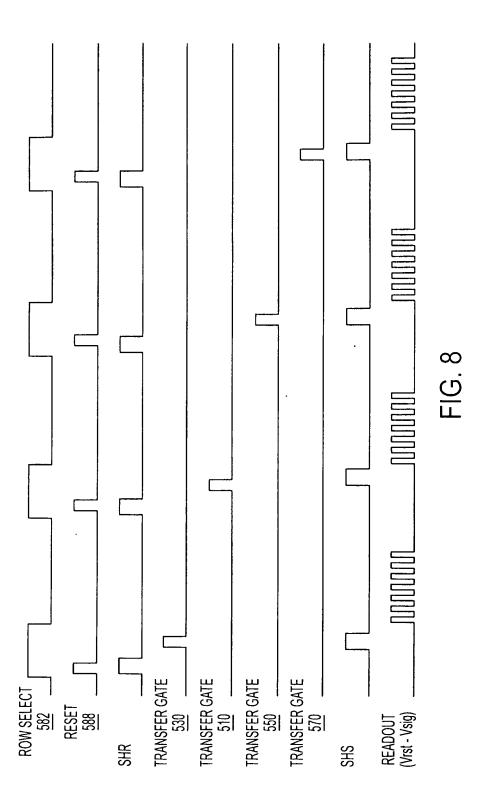


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Inventor: Peter P. Altice, Jr. et al.
Title: CMOS IMAGER WITH A CAPACITIVE STORAGE NODE LINKED TO

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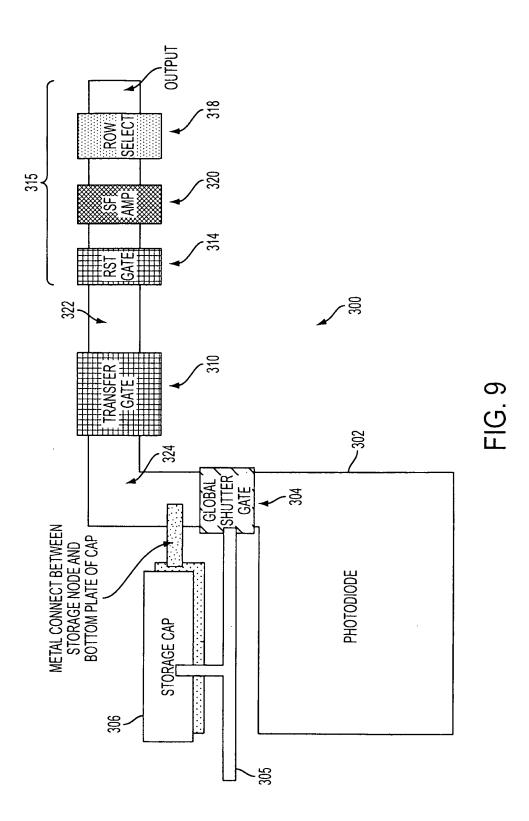
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nventor: Peter P. Altice, Jr. et al.

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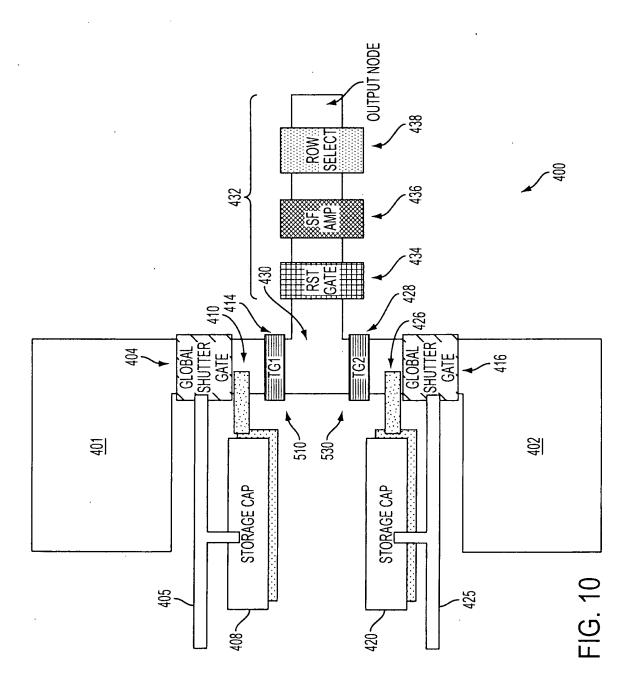
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Title: CMOS IMAGER WITH A CAPACITIVE STORAGE NODE LINKED TO

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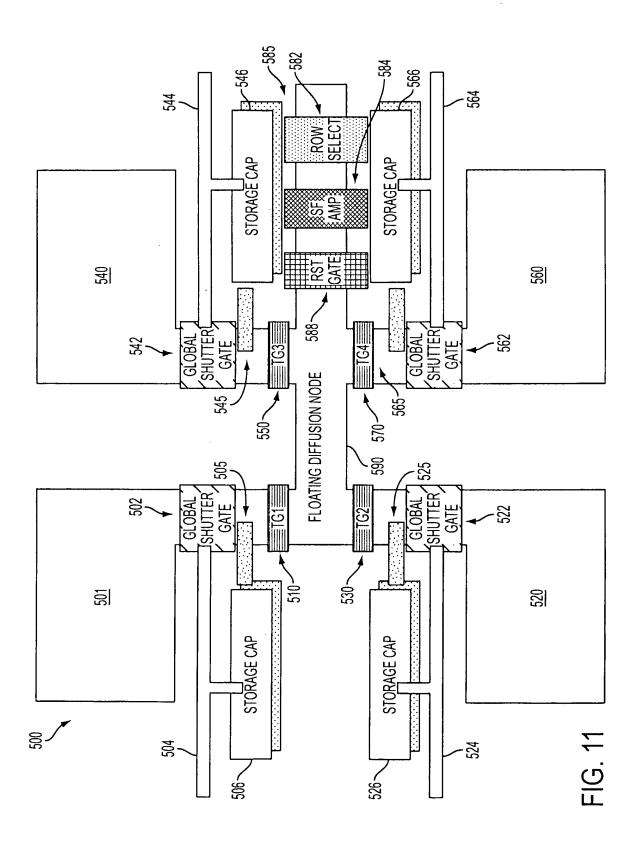
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App No.: 10/721,190 Docket No.: M4065.0852/P85 nventor: Peter P. Altice, Jr. et al.

Fitle: CMOS IMAGER WITH A CAPACITIVE STORAGE NODE LINKED TO Docket No.: M4065.0852/P852

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